

*Fig. 1*

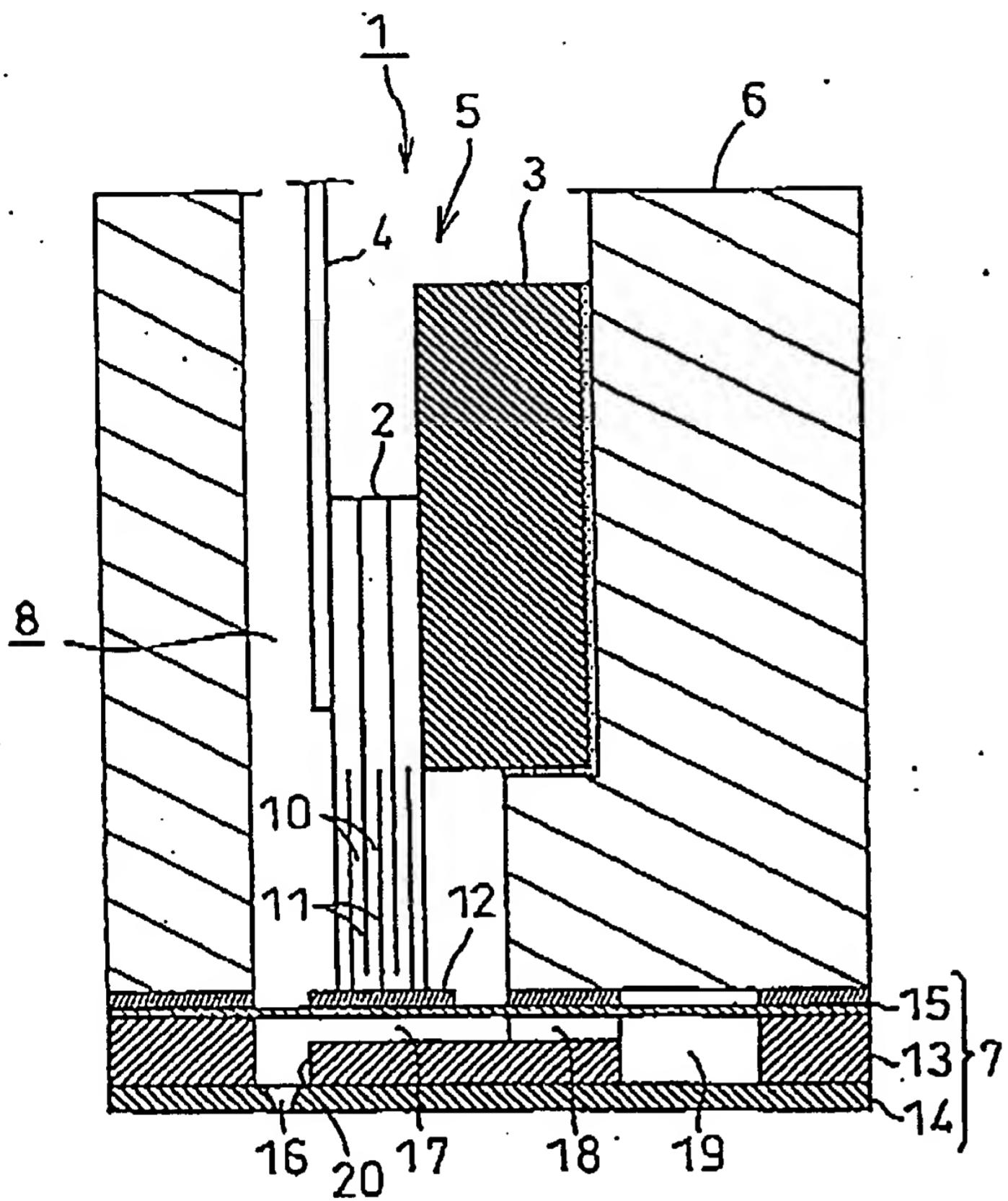


Fig. 2

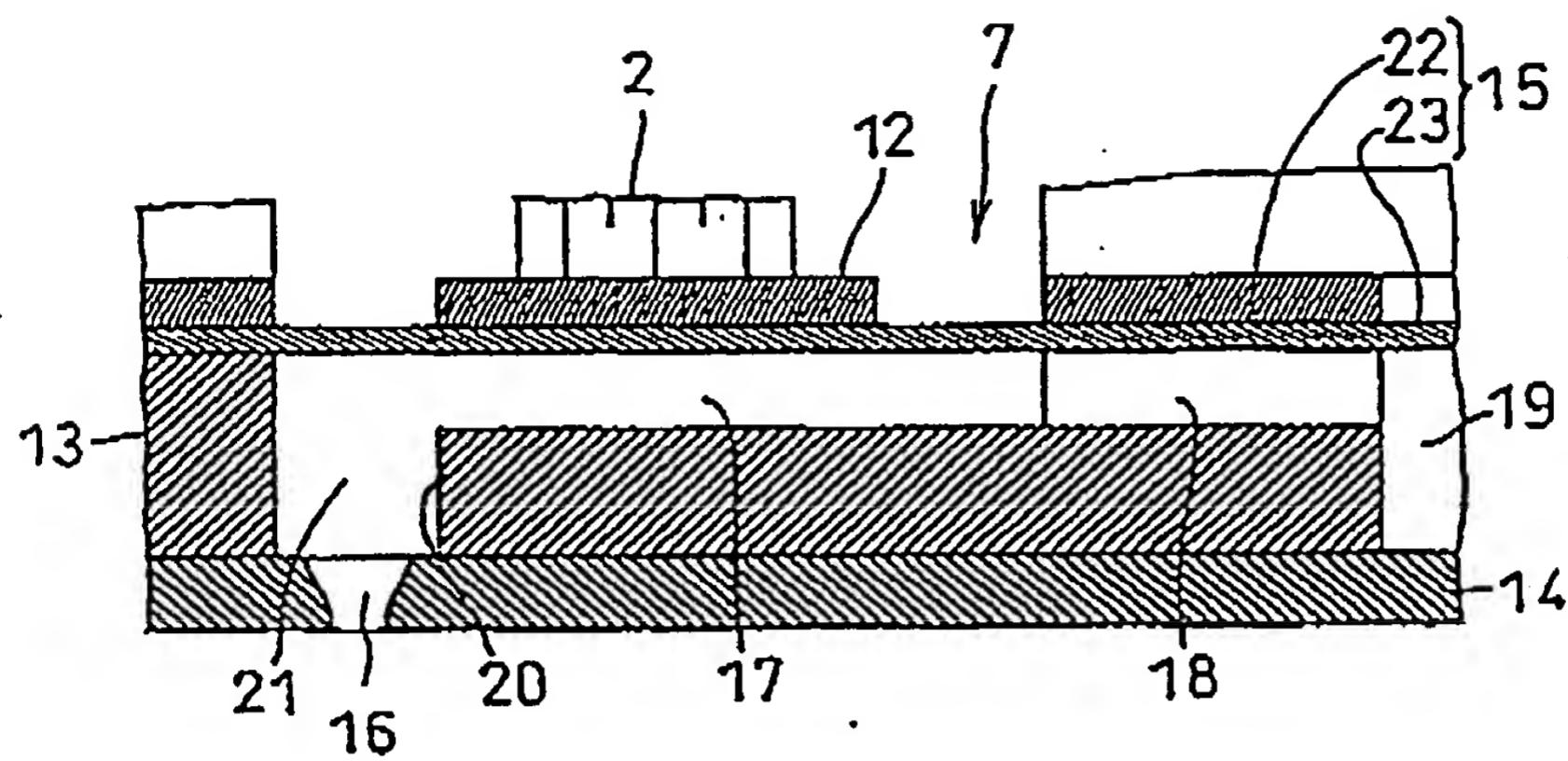
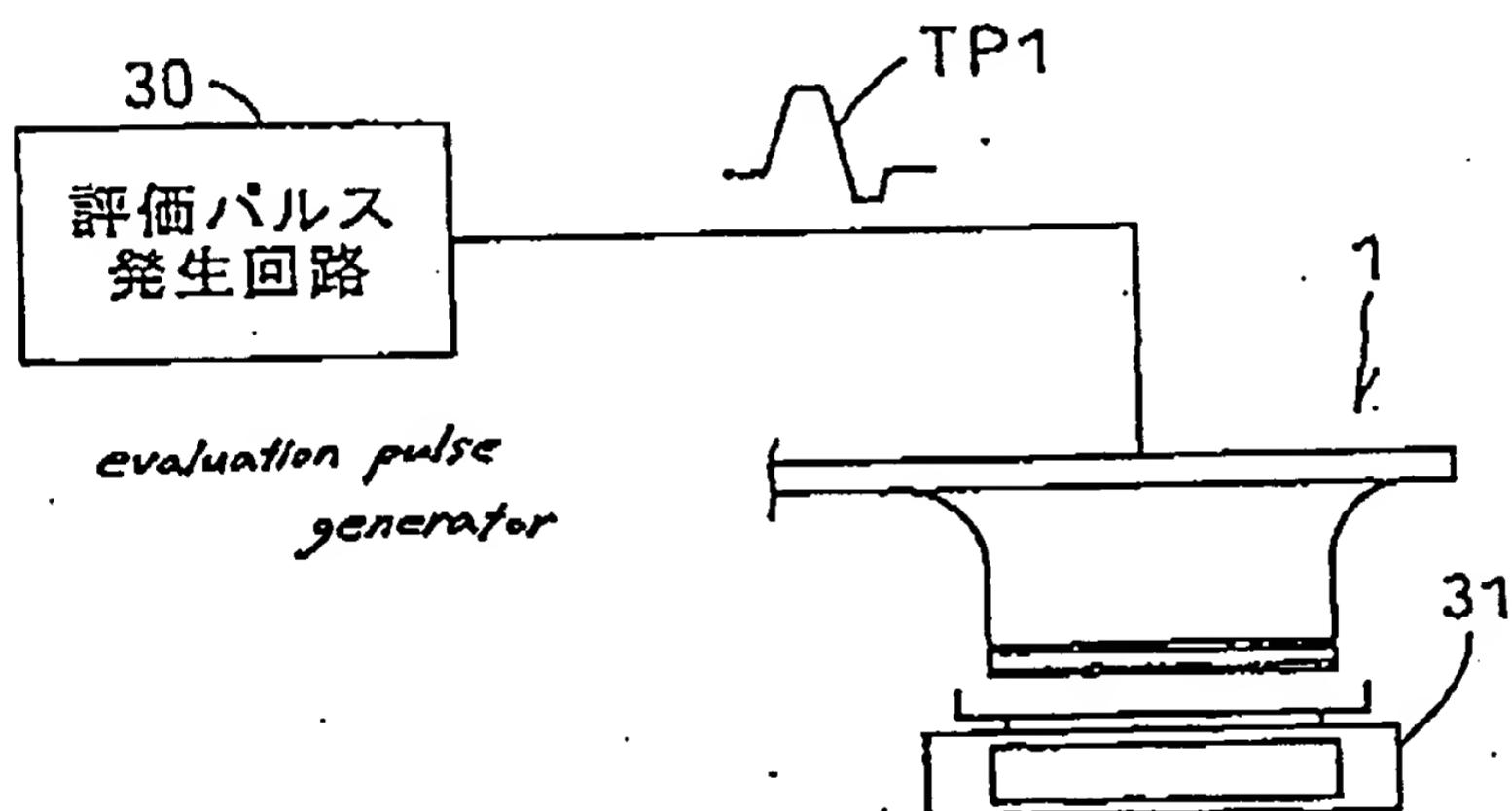


Fig. 3



00044664 00044665

Fig. 4

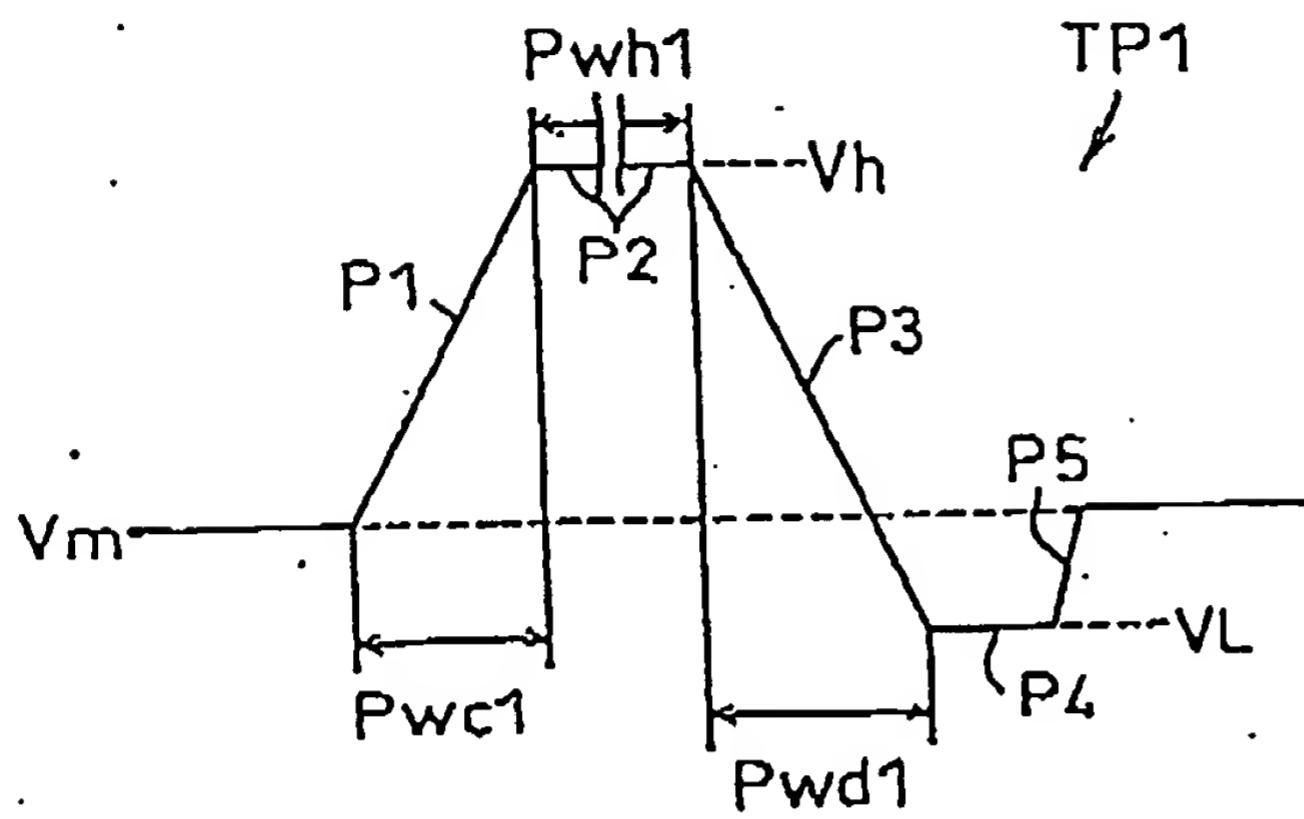


Fig. 5

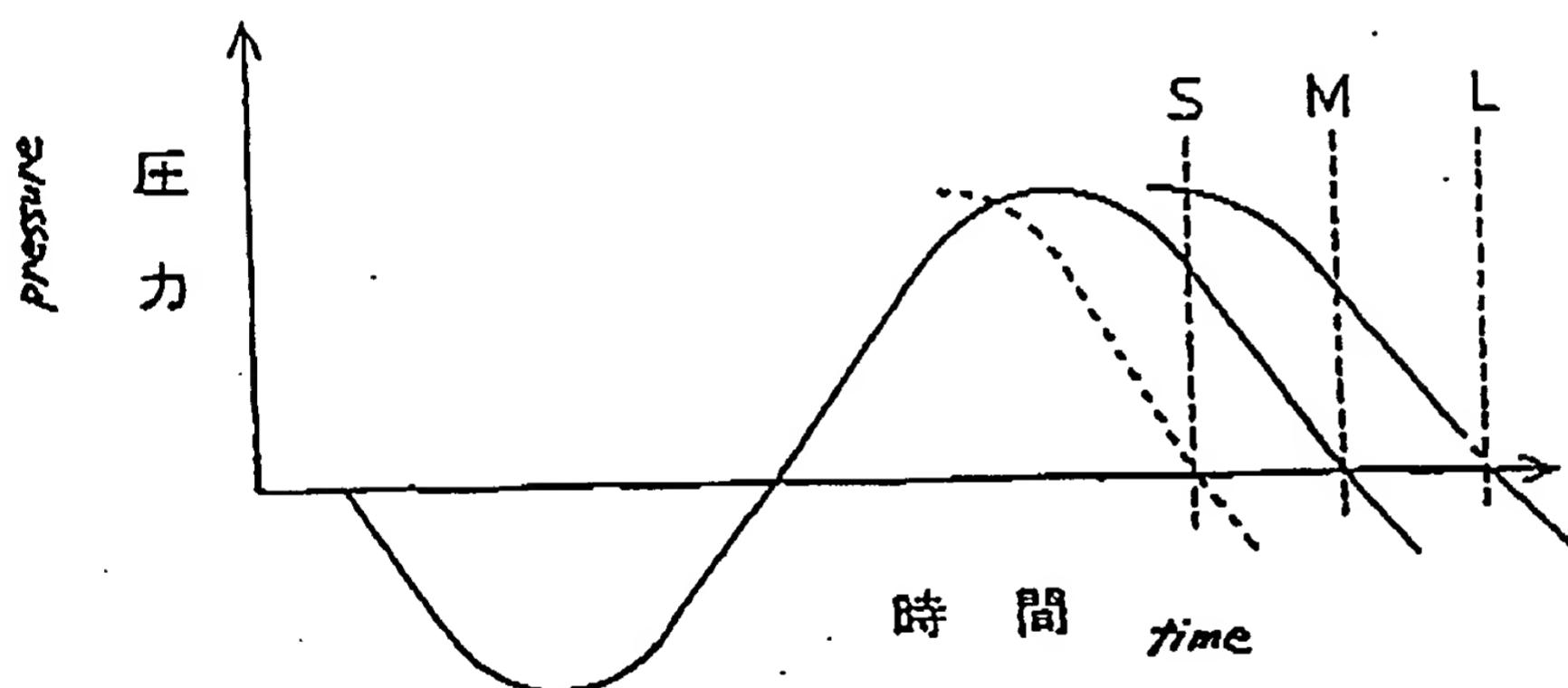
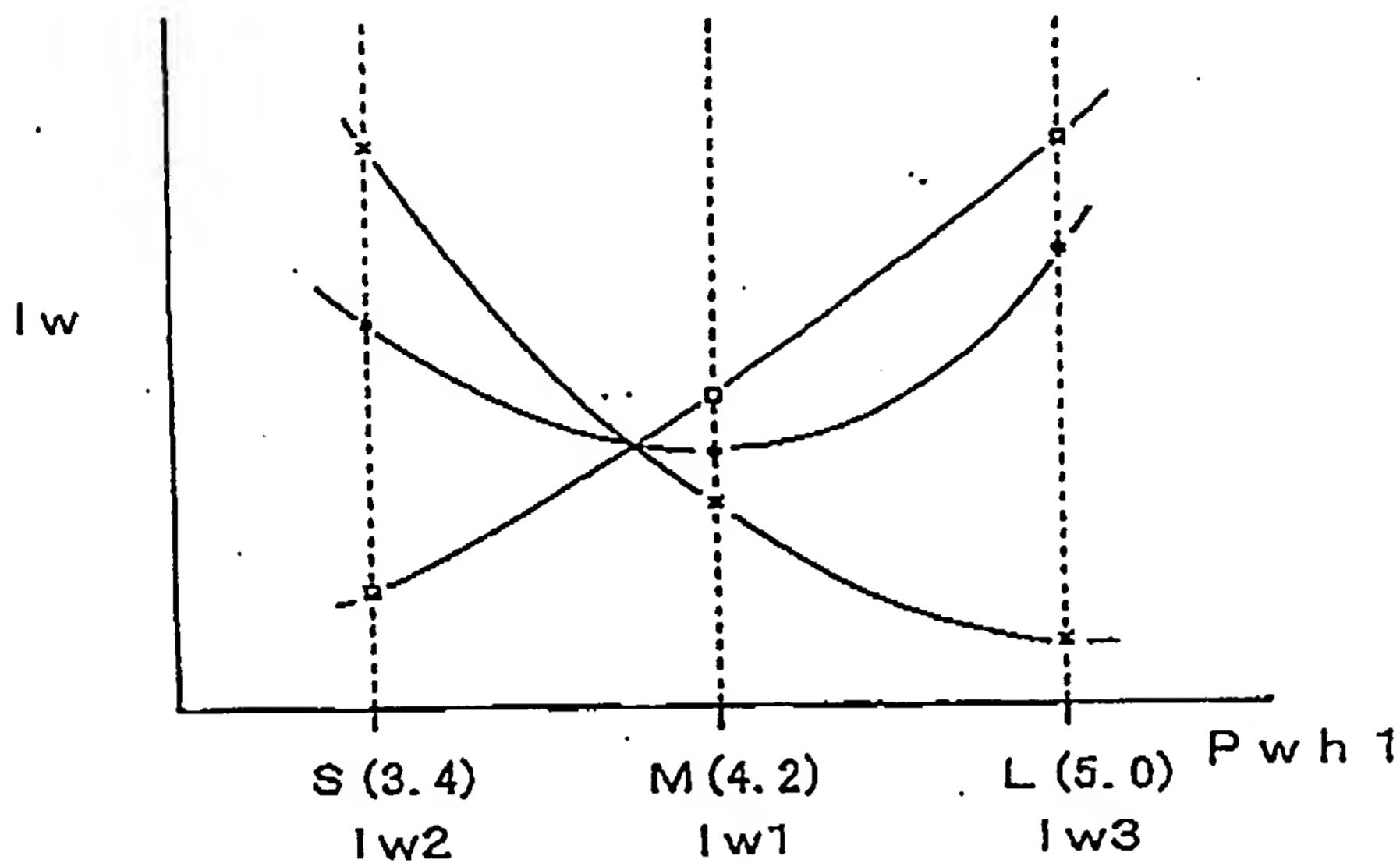


Fig. 6



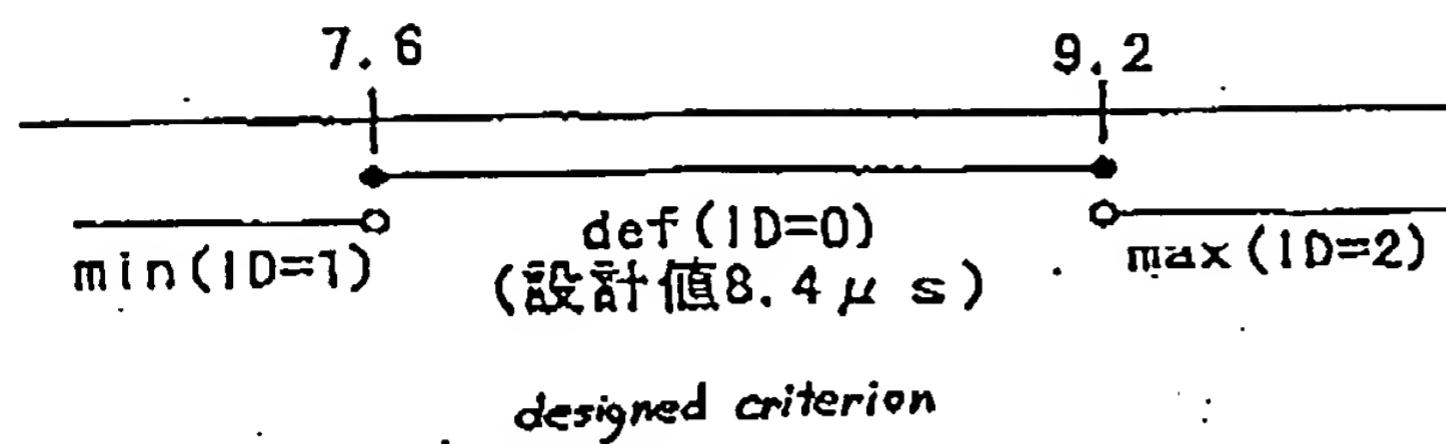
TECHNICAL DRAWING

Fig. 7

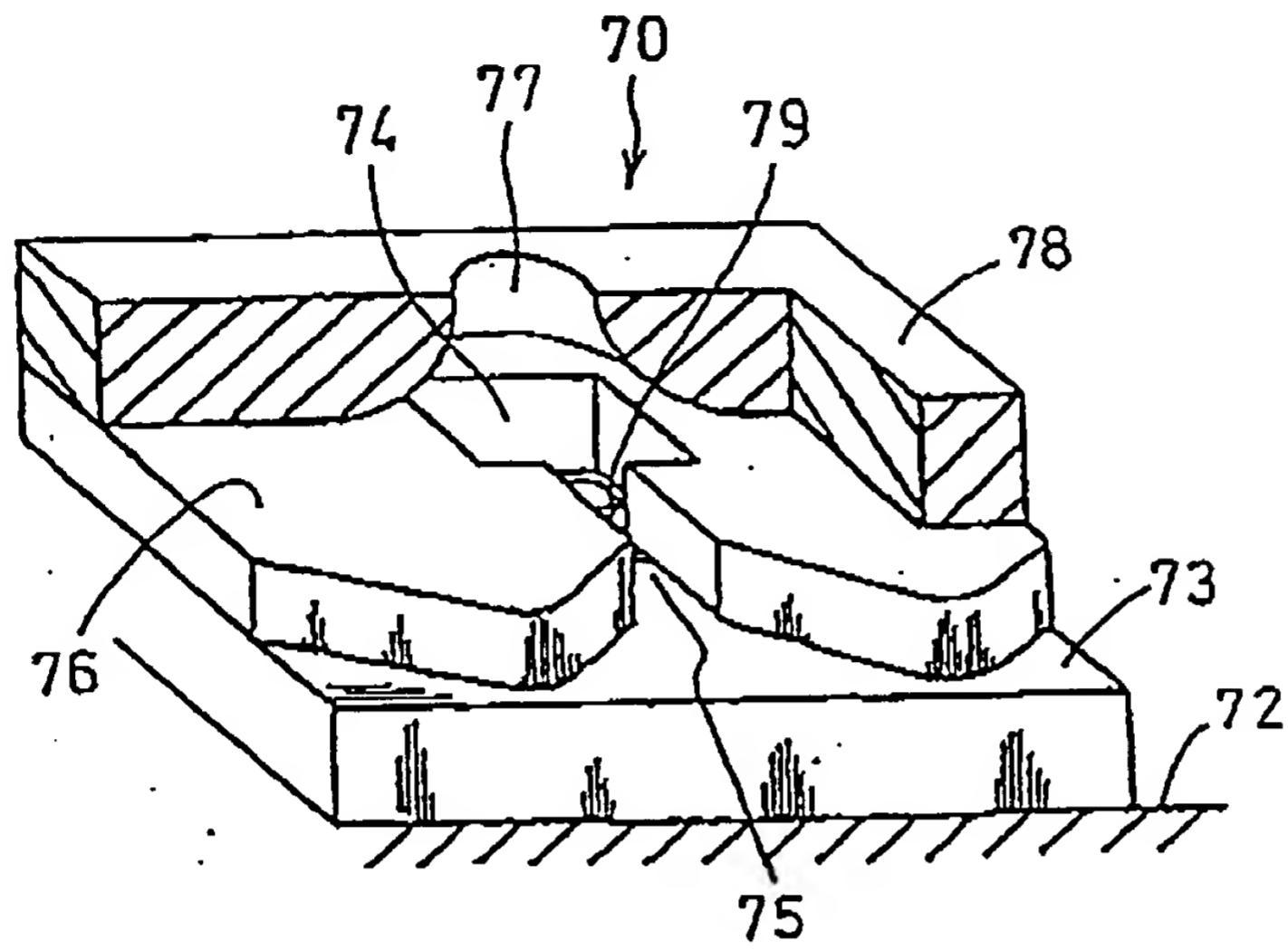
		$l_w$
$T_c$	rank ID	
ランク	0	$l_w2 > l_w1 < l_w3$
	1	$l_w2 > l_w1 = l_w3$
	2	$l_w2 = l_w1 < l_w3$
ID	エラーランク	$l_w2 < l_w1 < l_w3$
	1	$l_w2 > l_w1 > l_w3$
	2	$l_w2 = l_w1 = l_w3$
	3	$l_w2 < l_w1 > l_w3$
	4	$l_w2 < l_w1 > l_w3$

$T_c$  rank ID error

Fig. 8



*Fig. 9*



*Fig. 10*

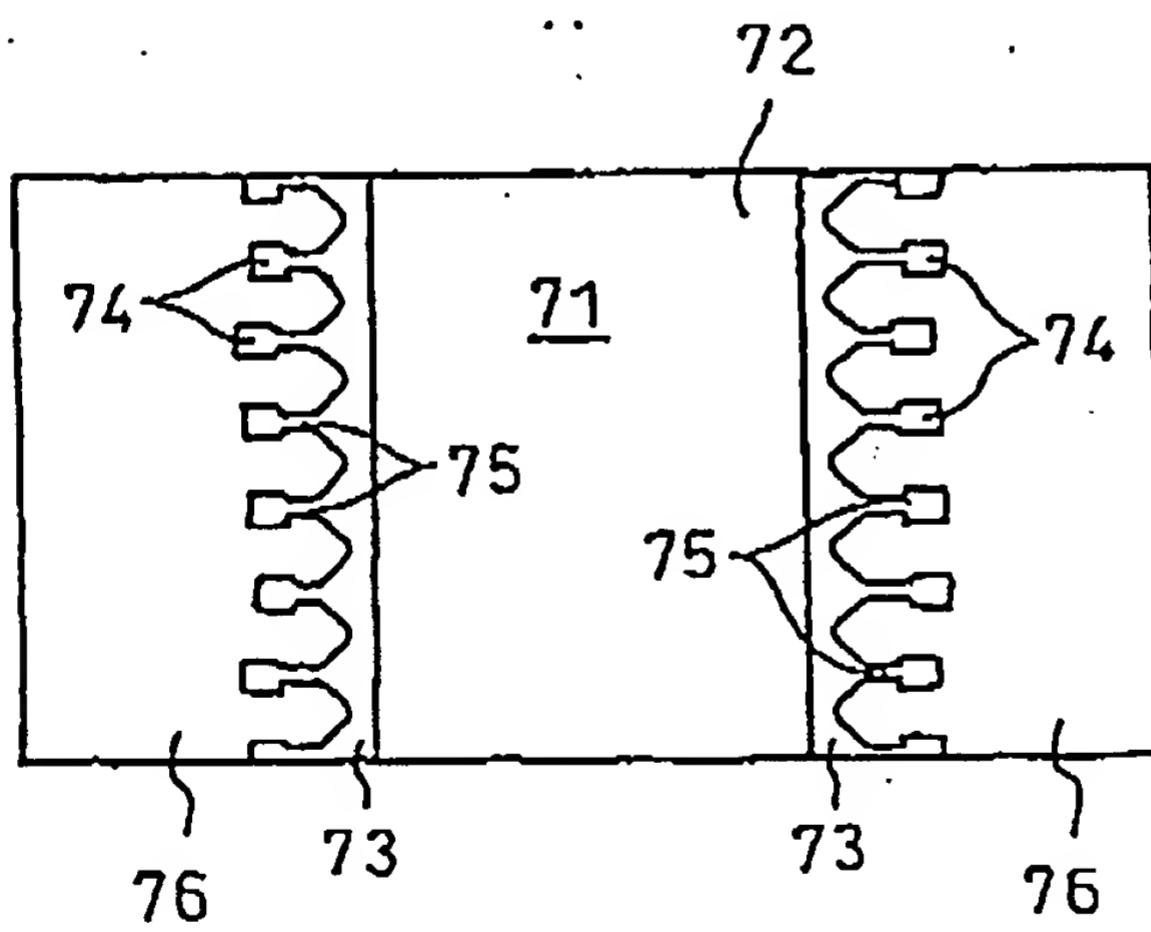


Fig. 11

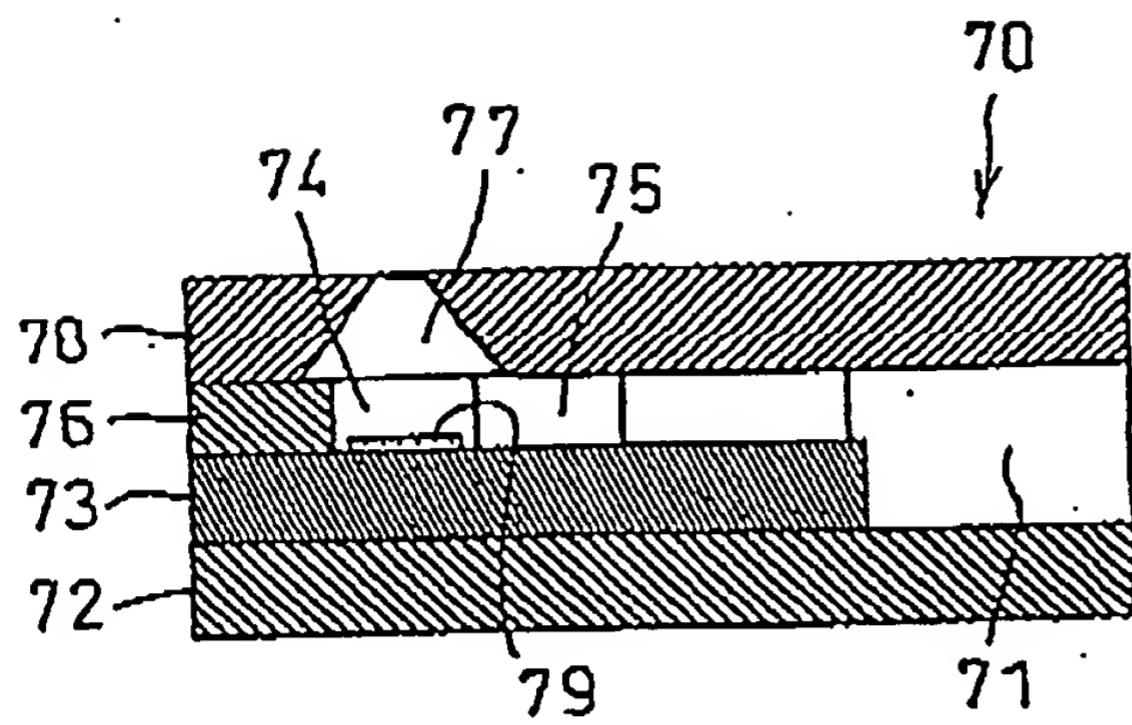


Fig. 12A

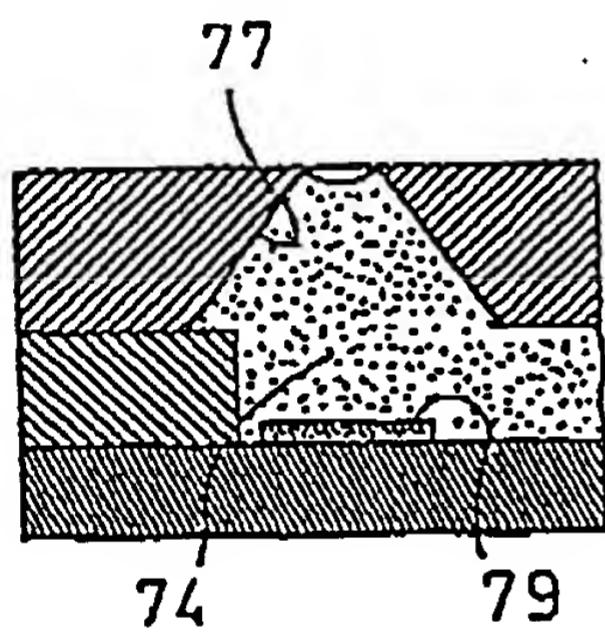


Fig. 12B

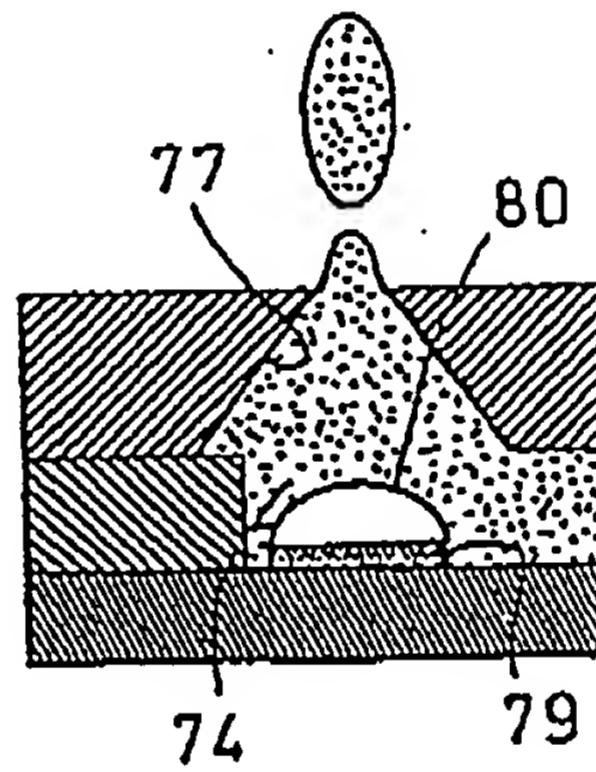


Fig. 13

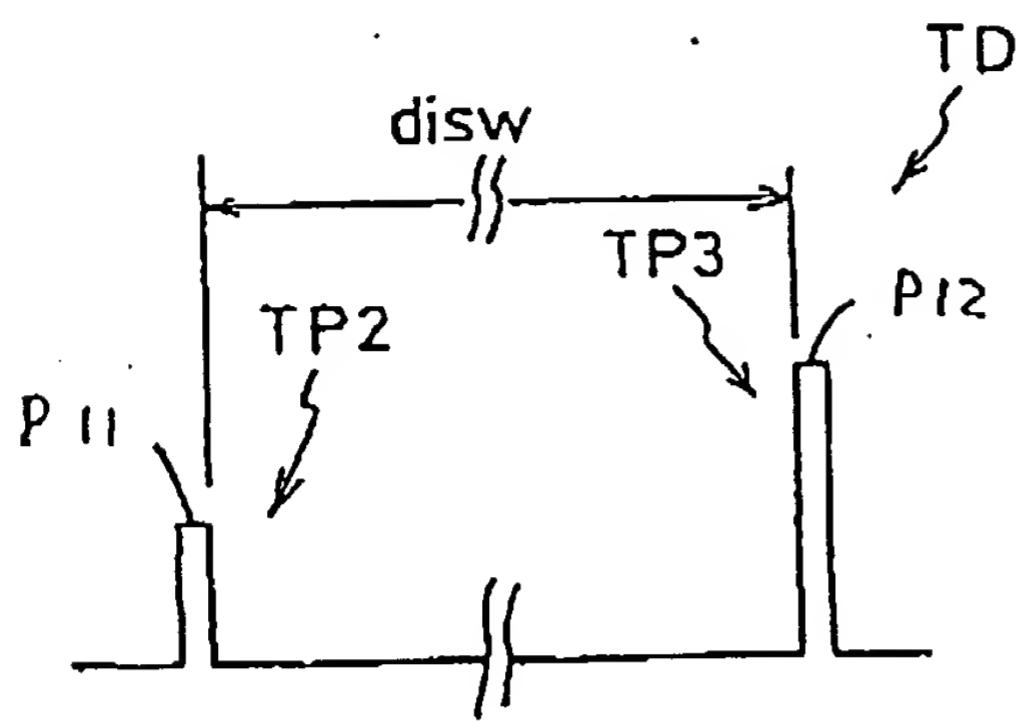
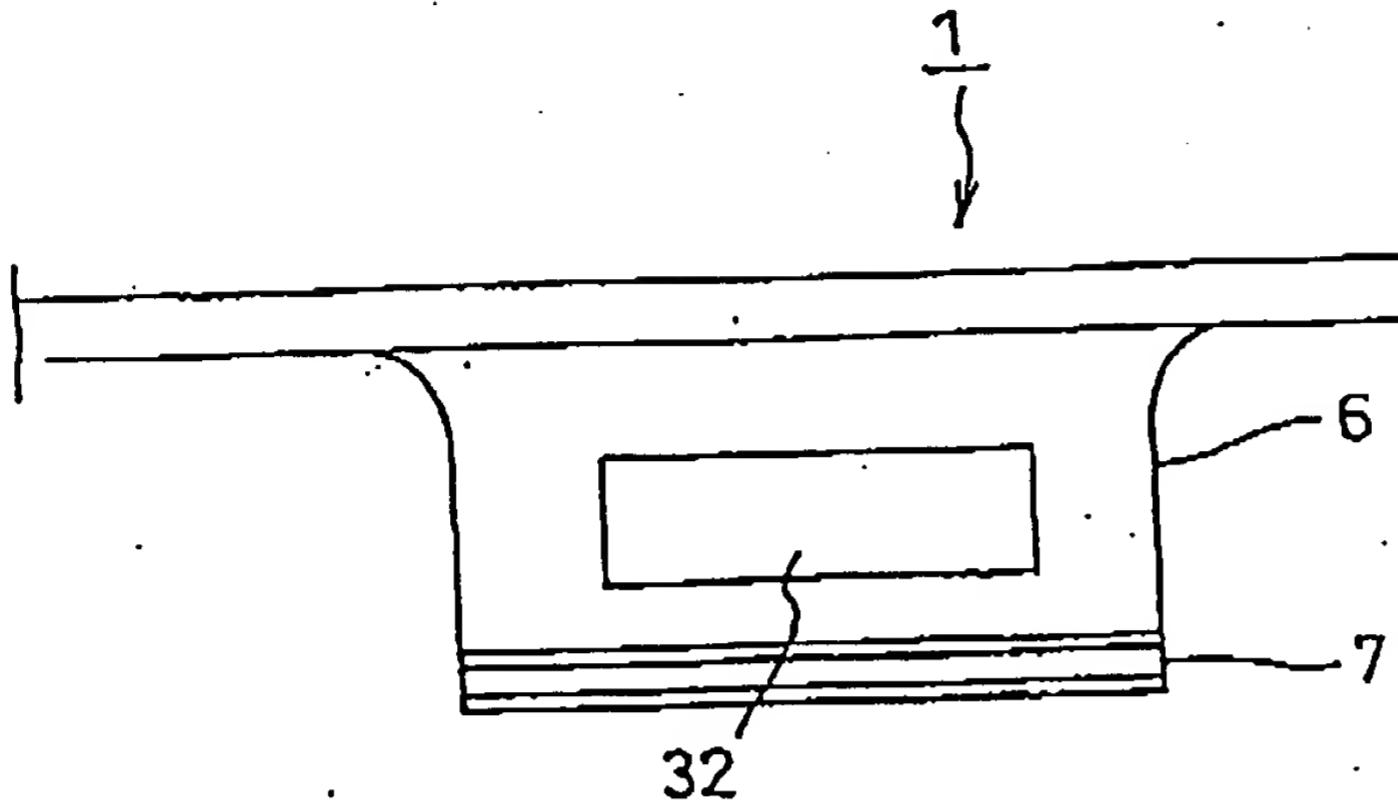
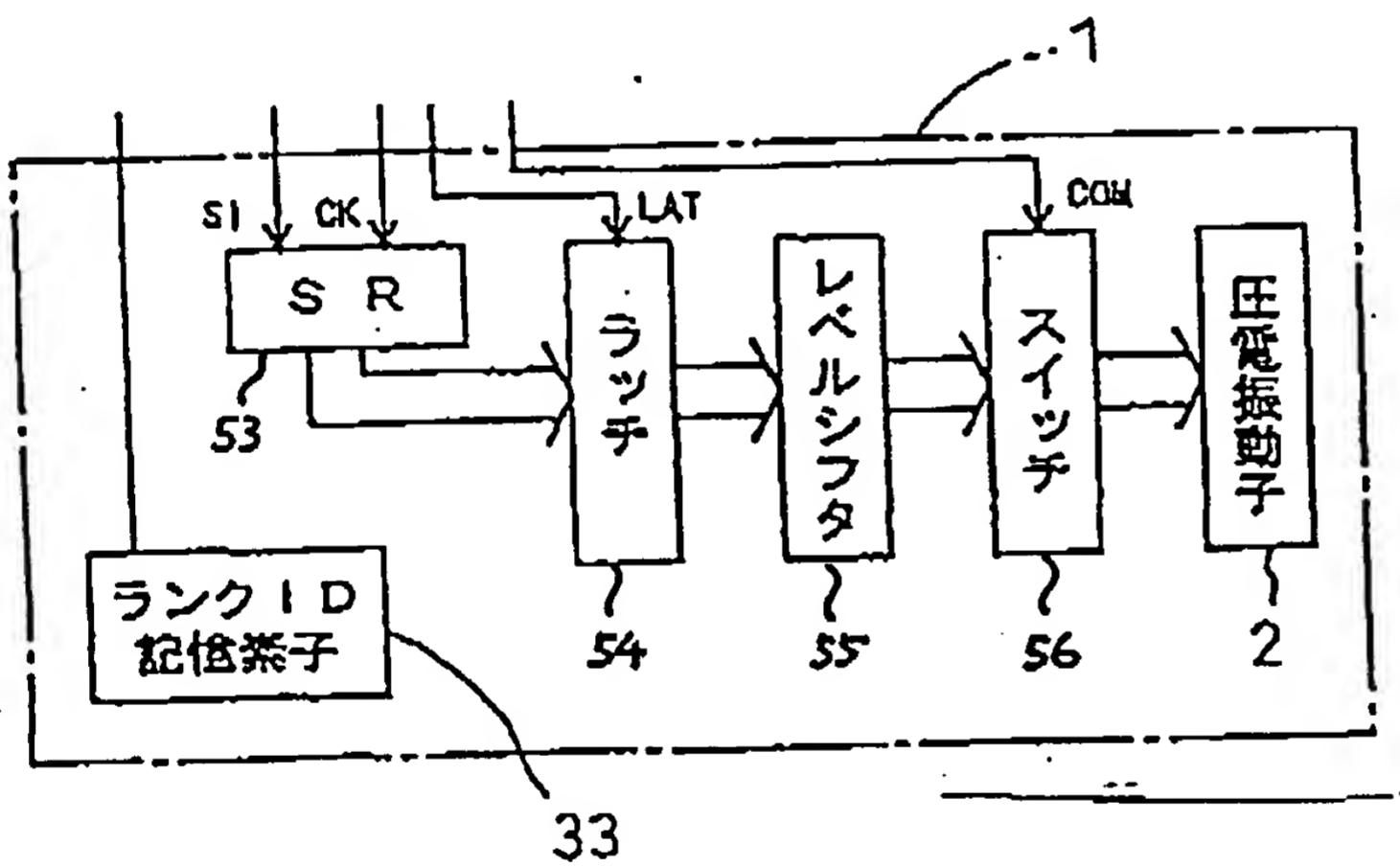


Fig. 14



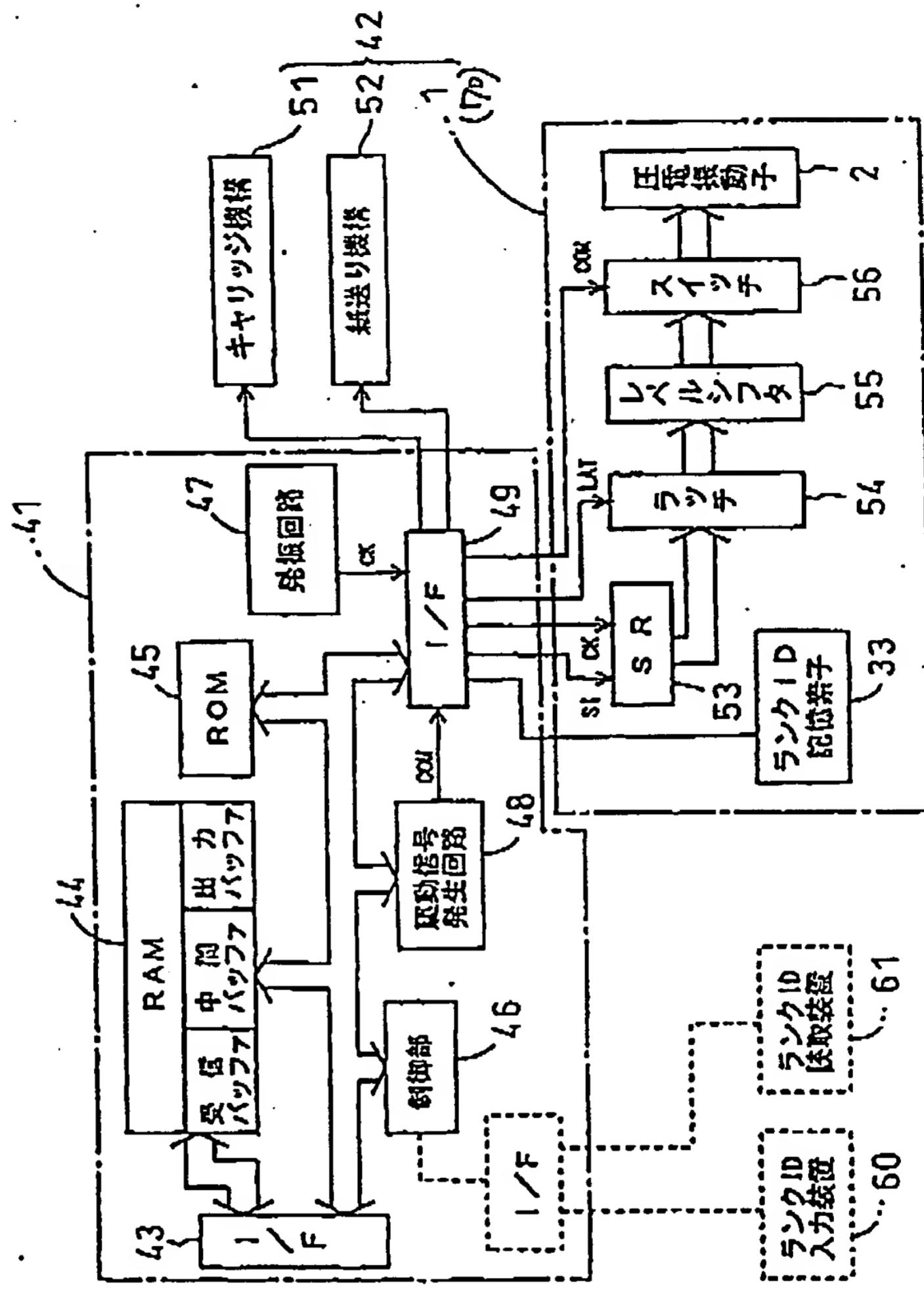
*Fig. 15*



- 2: piezoelectric vibrator
- 33: rank memory
- 53: shift register
- 54: latch
- 55: level shifter
- 56: switcher

卷之三

Fig. 16



- 2: piezoelectric vibrator
- 33: rank ID memory
- 44: reception buffer
- intermediate buffer
- output buffer
- 46: controller
- 47: oscillator
- 48: drive signal generator
- 51: carriage mechanism
- 52: paper feeding mechanism
- 53: shift register
- 54: latch
- 55: level shifter
- 56: switcher
- 60: rank ID input device
- 61: rank ID reader

TELEGRAM  
19.7.1960

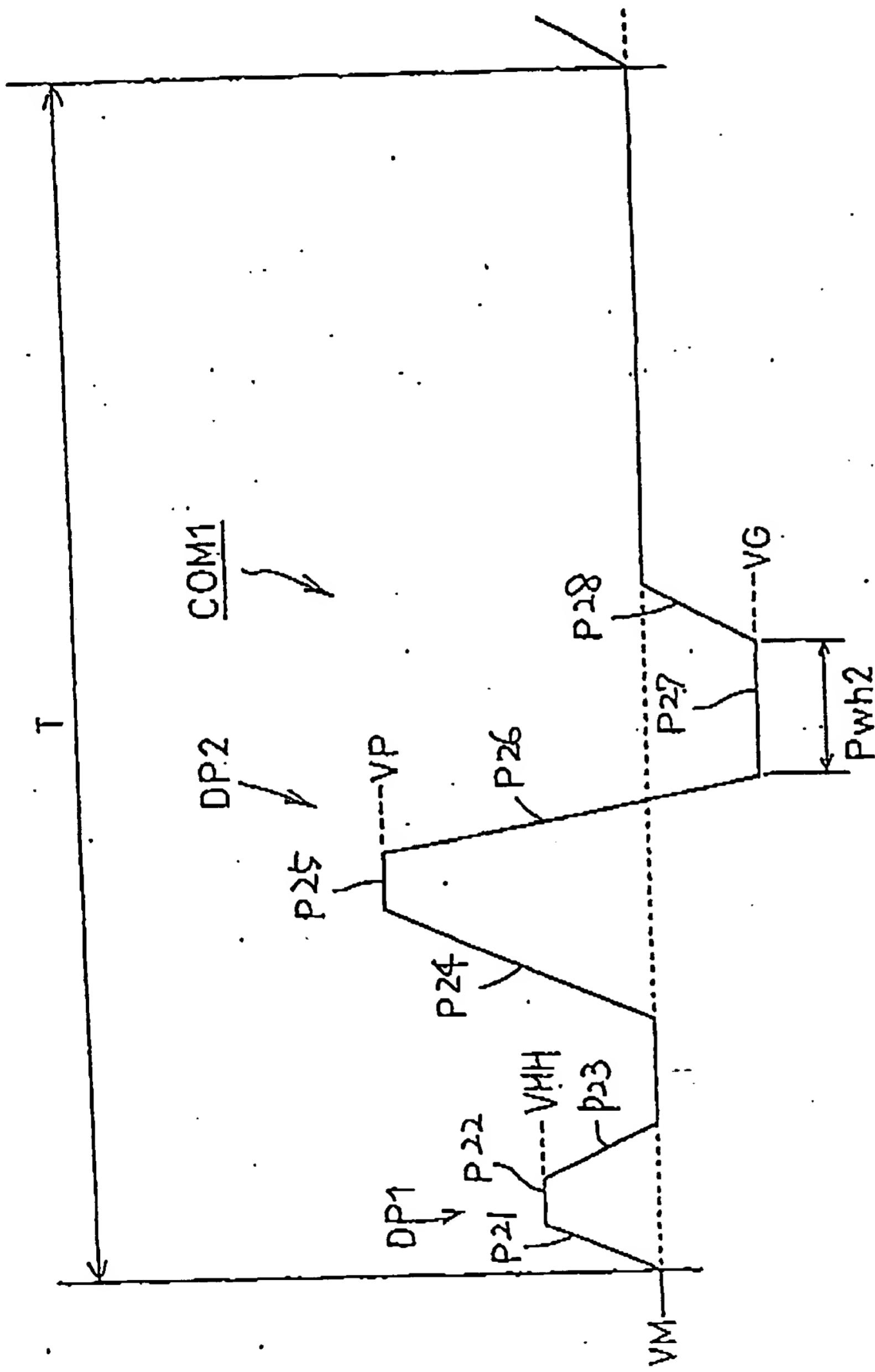


Fig. 18

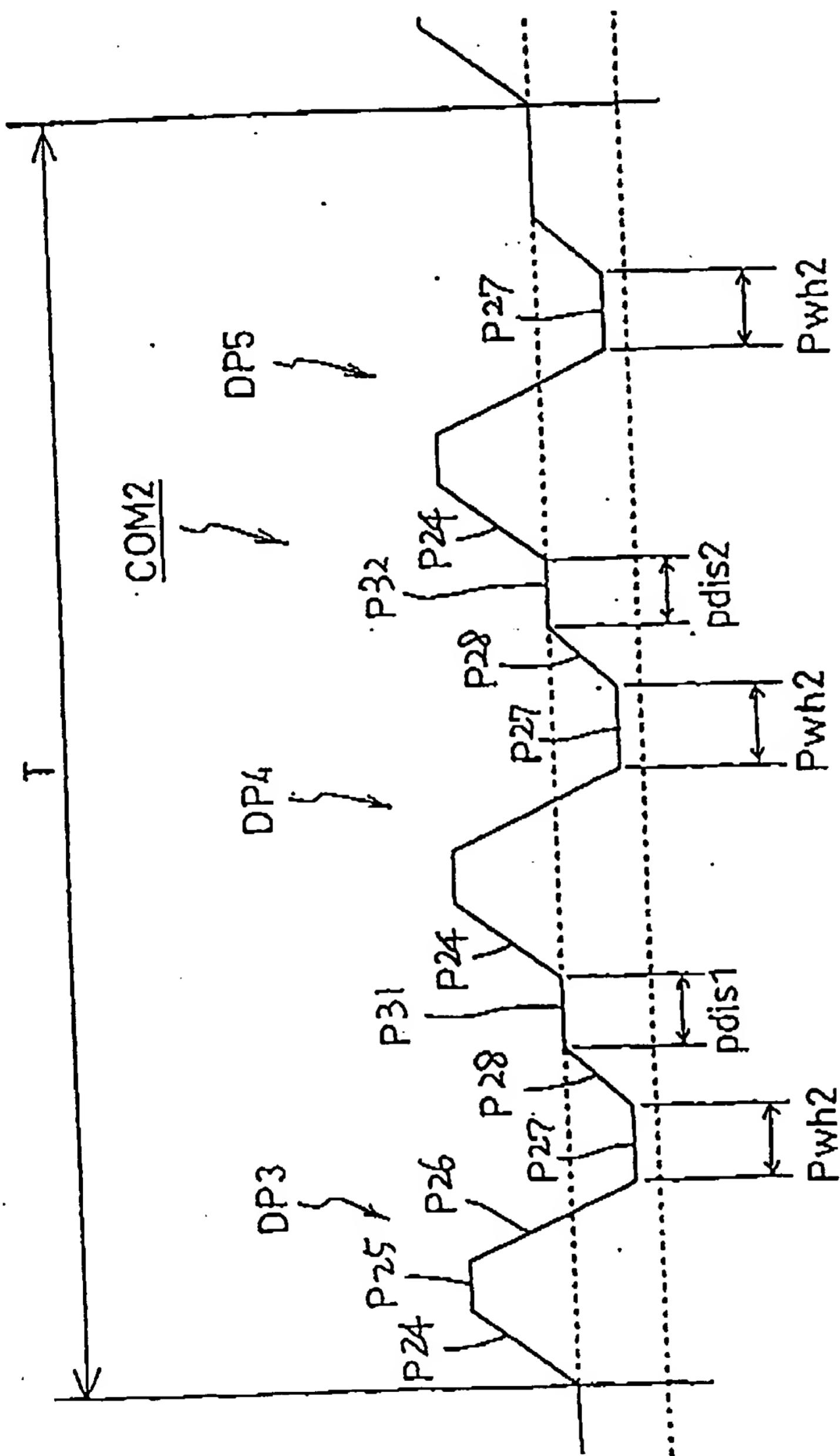


Fig. 219

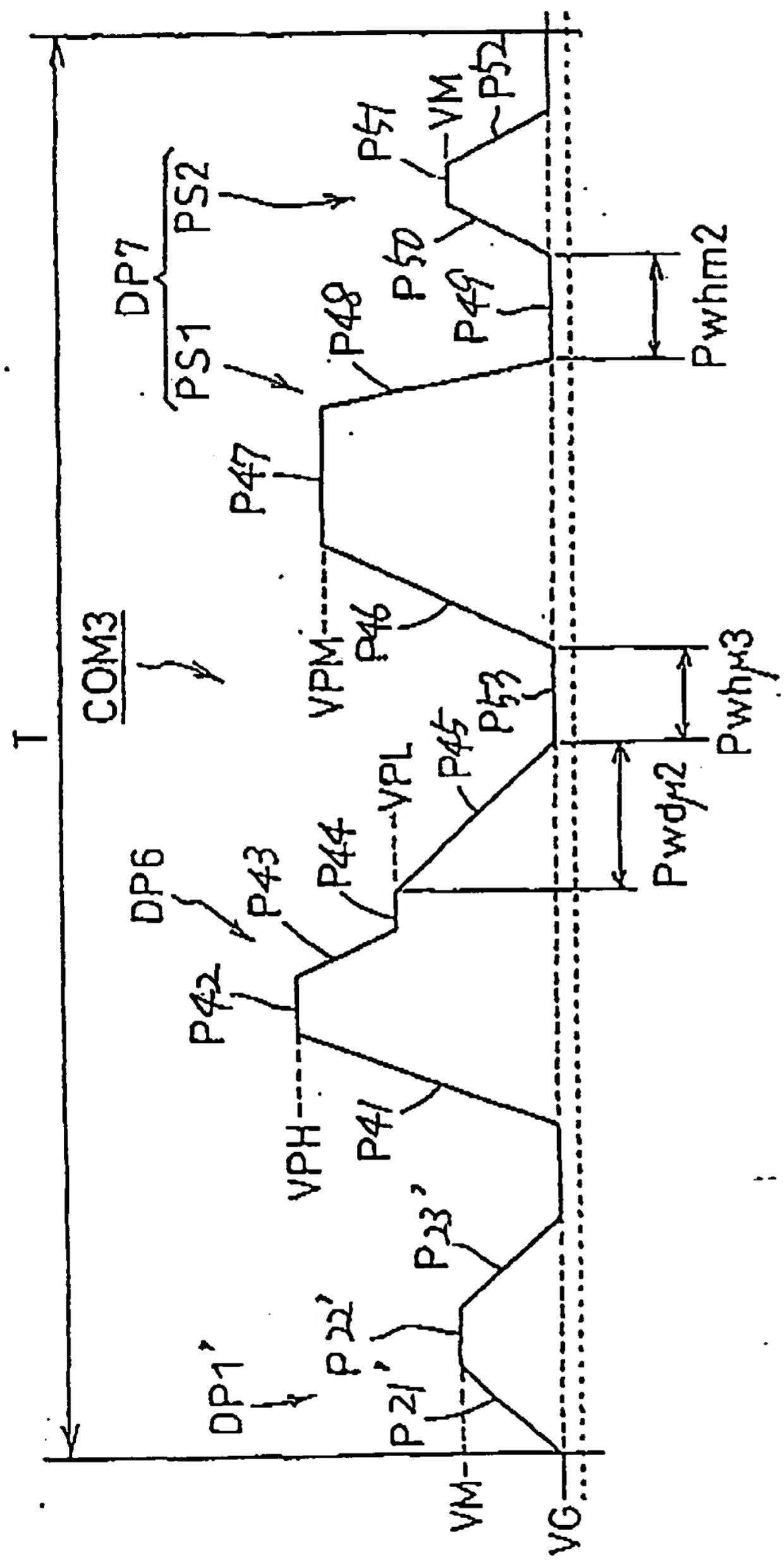


Fig. 20

COH4

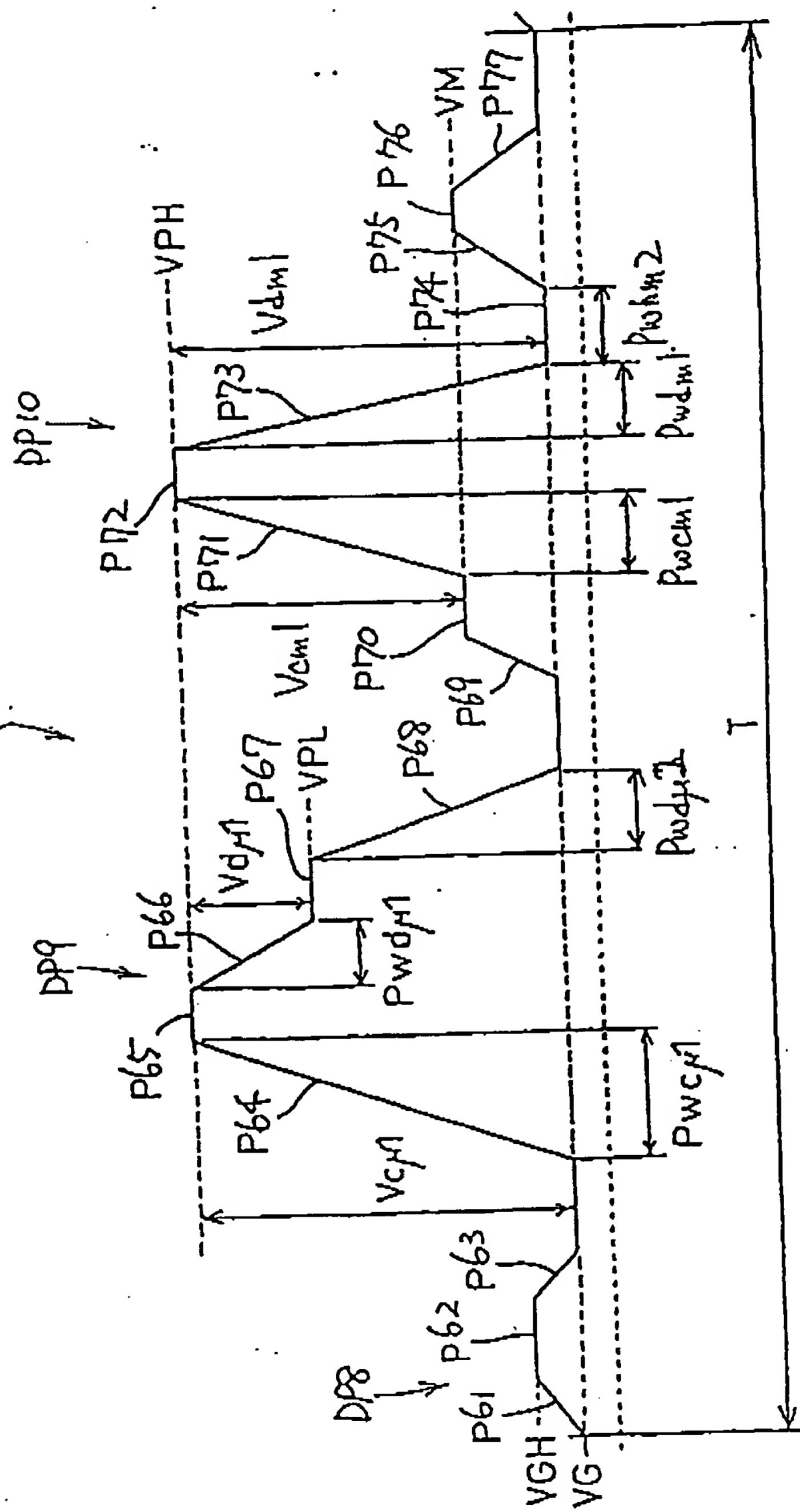


Fig. 21

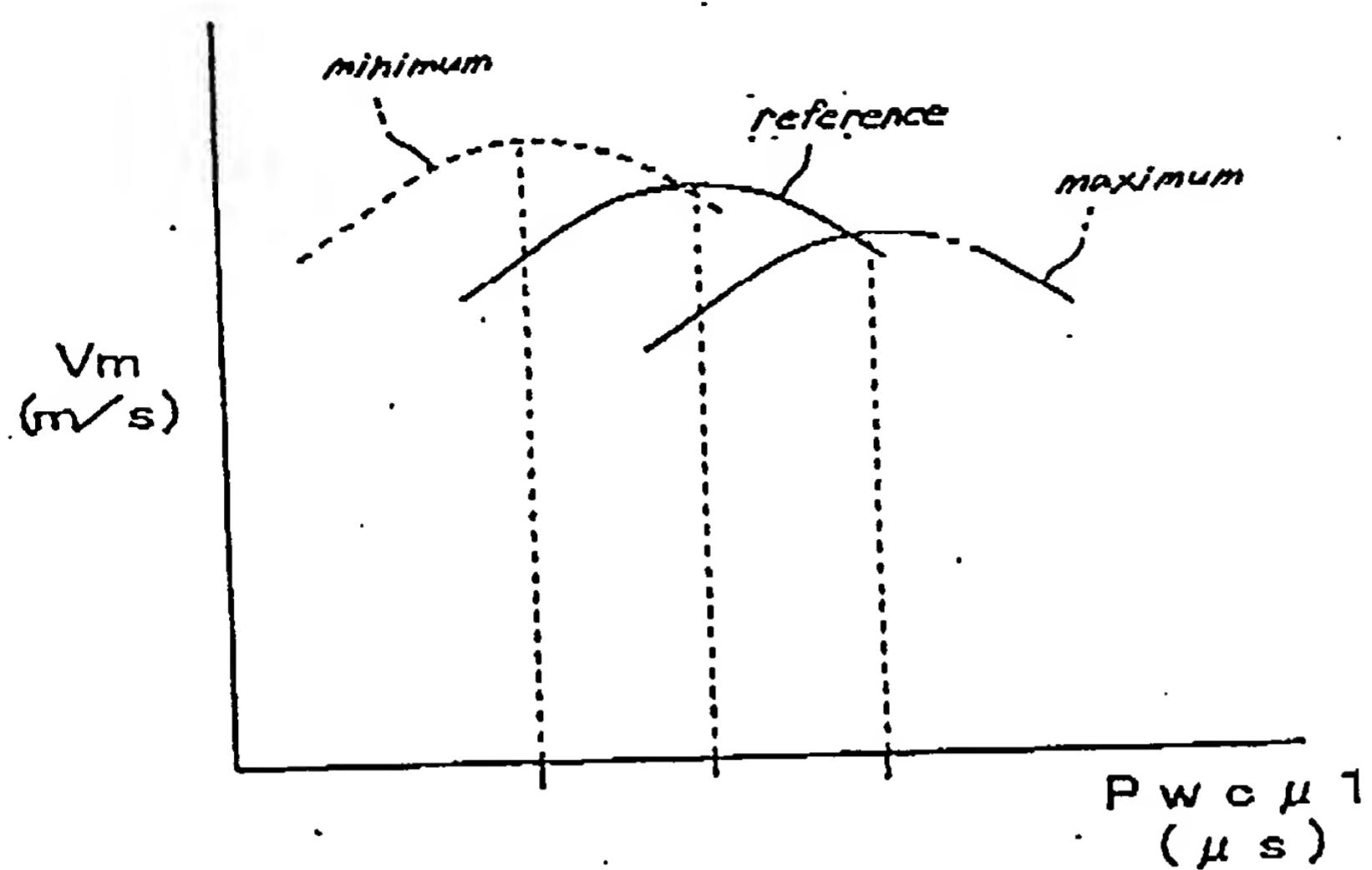
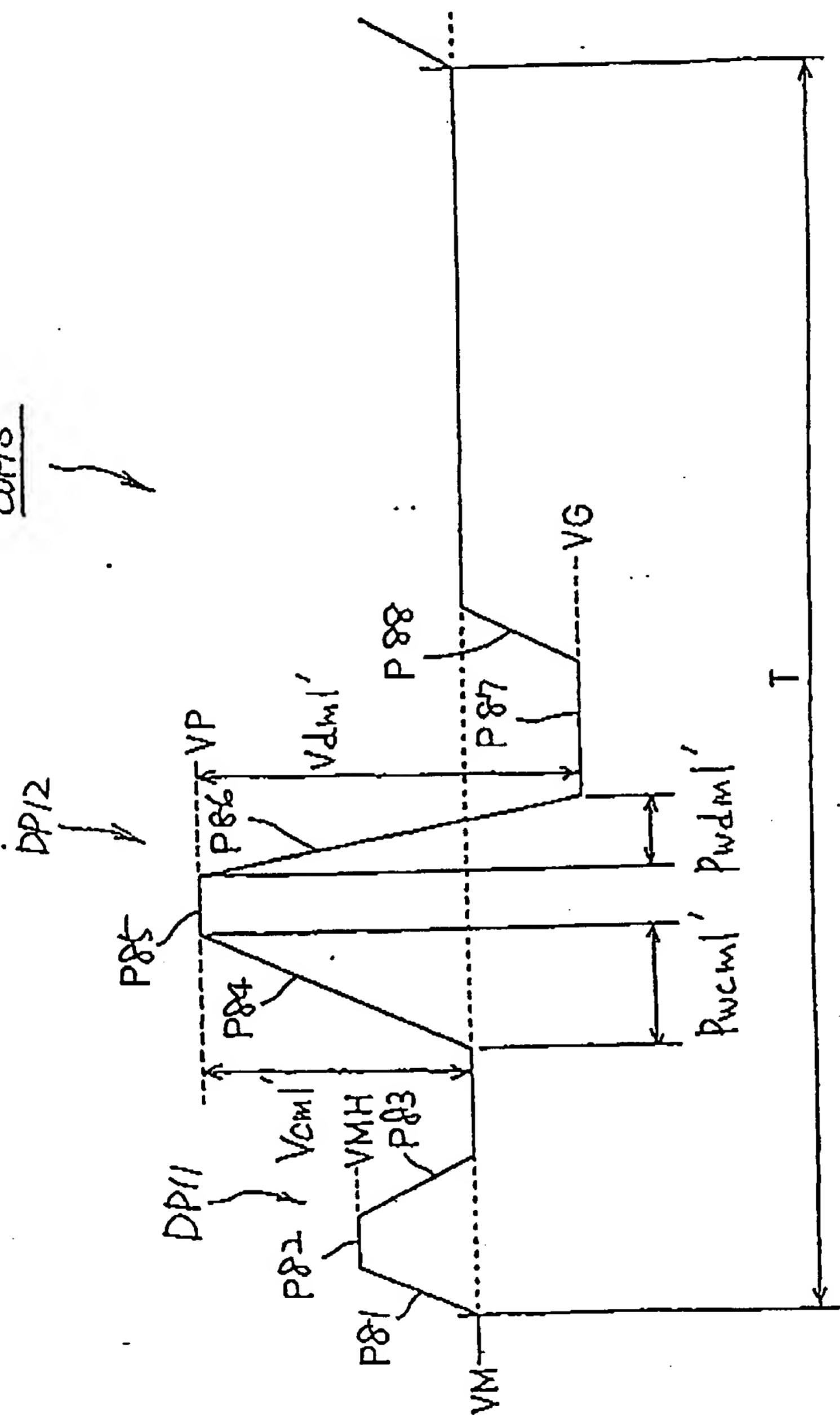
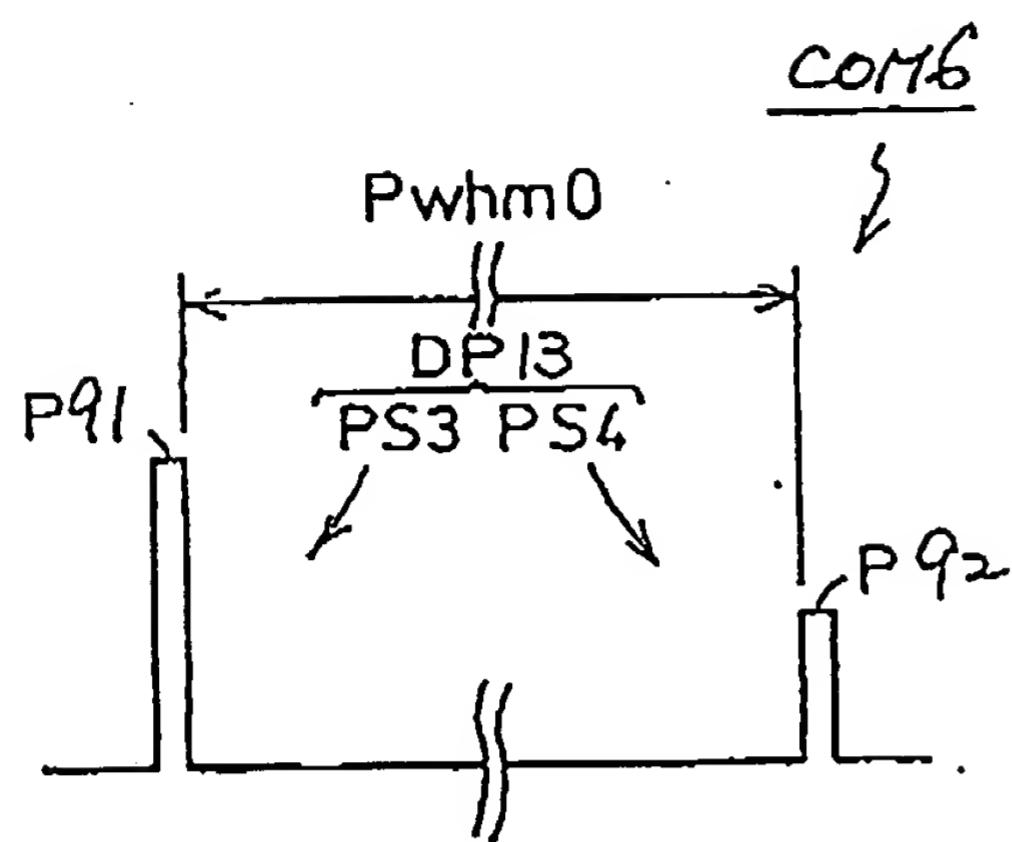


Fig. 22  
TDFE 80 " Fig. 22

cont



*Fig. 23*



*Fig. 24*

